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The presence of active bipolar transistors in a BiCMOS technology increases the latchup susceptibility compared to pure CMOS processes, due to the injection of

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The pnp and npn transistors can be natural to the technology, or parasitic devices. In CMOS technology, testing and tools. 1.1.1 CMOS Latchup What is Latchup?

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Jun 17, 2012 The CMOS structure is analyzed and the PNP and NPN latching transistors are identified. The CMOS latch schematic is drawn and the triggering methods are

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The Effects of Angle of Incidence and Temperature on Latchup in 65nm Technology J.M. Hutson 1, J.D. Pellish , G. Boselli2, R. Baumann2, R.A. Reed 1, R.D. Schrimpf , R

New transient characterization of latchup

This paper presents an analysis of the latchup development in CMOS integrated circuits. It sets out the time resolution of the triggering relevant to the differ

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All CMOS ICs have latch-up paths, but there are several design techniques that reduce susceptibility to latchup. [5] [6] [7] In CMOS technology, there are a number of

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This paper is a review of the latchup phenomena in past and present CMOS technologies. Both static and transient characterization techniques are described, as well as

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Latchup -

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The use of CMOS technology in space needs a careful evaluation of the latchup risk. The radiation tolerance is studied here for a standard 1.0 [μ]m high density

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Jan 11, 2009 Lecture Series on VLSI Design by Dr.Nandita Dasgupta, Department of Electrical Engineering, IIT Madras. For more details on NPTEL visit

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Interest in latchup is being renewed with the evolution of complimentary metal-oxide semiconductor (CMOS) technology, metal-oxide-semiconductor

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A latch-up is a type of short circuit which can occur in an integrated circuit (IC). More specifically it is the inadvertent creation of a low- impedance path between

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Cmos - wikipedia, the free encyclopedia

Complementary metal oxide semiconductor (CMOS) is a technology for constructing integrated circuits. CMOS technology is used in microprocessors, microcontrollers

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What is cmos latch up - answers.com

What is CMOS latch up? cmos technology is used in low power applications in vlsi and also CMOS is an acronym for complementary metal oxide semiconductor,

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